

39. The method of claim 35 wherein:

latching the first data bit comprises latching the first data bit in the master latch after
powering up the integrated circuit; and

loading the first data bit into the slave latch comprises loading the first data bit into the
slave latch after powering up the integrated circuit

REMARKS

Claims 1 - 39 are pending in this broadening reissue application. The Applicant has amended claims 18, 23, 35, and 36 and has added new claims 38 and 39.

The Applicant has added no new matter to the reissue application.

In light of the foregoing, claims 1 - 17, 19 - 22, 24 - 34, and 37 as previously pending, claims 18, 23, and 35 - 36 as amended, and new claims 38 - 39 are in condition for full allowance, and that action is respectfully requested.

If the Examiner believes that a phone interview would be helpful, he is respectfully requested to contact the Applicant's attorney, Bryan Santarelli, at (425) 455-5575.

DATED this 18th day of October, 2000.



Respectfully submitted,

GRAYBEAL JACKSON HALEY LLP


Bryan A. Santarelli

Attorney for Applicants

Registration No. 37,560

155-108th Avenue N.E., Ste 350

Bellevue, WA 98004-5901

(425) 455-5575 X

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